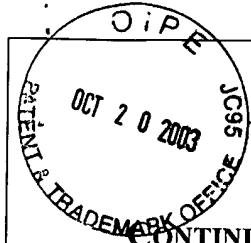


*Imay**RCE/2800**15*

**REQUEST  
FOR  
CONTINUED EXAMINATION (RCE)  
TRANSMITTAL**

Subsection (b) of 35 U.S.C. § 132, effective on May 29, 2000, provides for continued examination of an utility or plant application filed on or after June 8, 1995.

See The American Inventors Protection Act of 1999 (AIPA).

<i>Application Number</i>	09/893023
<i>Filing Date</i>	June 27, 2001
<i>First Named Inventor</i>	Frank O'Mahony
<i>Group Art Unit</i>	2826
<i>Examiner Name</i>	Ahmed Sefer
<i>Attorney Docket Number</i>	884.405US1
<i>Customer No.</i>	21186
<i>Confirmation No.</i>	3406

This is a Request for Continued Examination (RCE) under 37 CFR § 1.114 of the above-identified application entitled **LOW LOSS INTERCONNECT STRUCTURE FOR USE IN MICROELECTRONIC CIRCUITS**.

Submission required under 37 C.F.R. § 1.114

1.  Consider the amendment(s)/reply under 37 C.F.R. § 1.116 previously filed on .
2.  Consider the arguments in the Appeal Brief or Reply Brief previously filed on .
3.  An Amendment Under 37 CFR § 1.116 ( 10 pages ) is enclosed.
4.  A new power of attorney (    pages ) is enclosed.
5.  An Information Disclosure Statement is enclosed (    pages ). 01-FE:1801 770.00 DA
  - a. Form(s) 1449
  - b. Copies of IDS Citations
6.  Authorization to charge Deposit Account No. 19-0743 in the amount of \$770.00 to pay the RCE filing fee required under C.F.R. § 1.17(e).
7.  **The Commissioner is hereby authorized to credit overpayments or charge any fees set forth in 37 CFR §§ 1.16 through 1.18 to Deposit Account No. 19-0743.**
8.  A petition for extension of time in the prior application (    pages ) is enclosed along with a check in the amount of to pay the extension fee.
9.  Others: Communication Concerning Related Patents (1 pg.).

10/21/2003 JADD01 00000069 09893023

SCHWEGMAN, LUNDBERG, WOESSNER & KLUTH, P.A.  
Customer Number: 21186

By: David R. Cochran  
Atty: David R. Cochran  
Reg. No. 46,632

**CERTIFICATE UNDER 37 CFR 1.8:** The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Attn-Mail Stop RCE, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 15 day of October, 2003.

Name Anne M. Richards

Signature Anne M. Richards



S/N 09/893023

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Frank O'Mahony et al.	Examiner:	Ahmed Sefer
Serial No.:	09/893023	Group Art Unit:	2826
Filed:	June 27, 2001	Docket:	884.405US1
Title:	LOW LOSS INTERCONNECT STRUCTURE FOR USE IN MICROELECTRONIC CIRCUITS		
Assignee:	Intel Corporation	Customer No.:	21186

COMMUNICATION CONCERNING RELATED PATENTS

Mail Stop RCE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Applicants would like to bring to the Examiner's attention the following related patents in the above-identified patent application:

<u>Patent No.</u>	<u>Serial No.</u>	<u>Filing Date</u>	<u>Attorney Docket</u>
6,522,186	09/893,067	June 27, 2001	884.431US1

Respectfully submitted,

FRANK O'MAHONY ET AL.

By Applicants' Representatives,

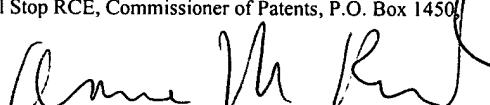
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Minneapolis, MN 55402  
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Date 15 October 2003 By \_\_\_\_\_

  
David R. Cochran  
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